

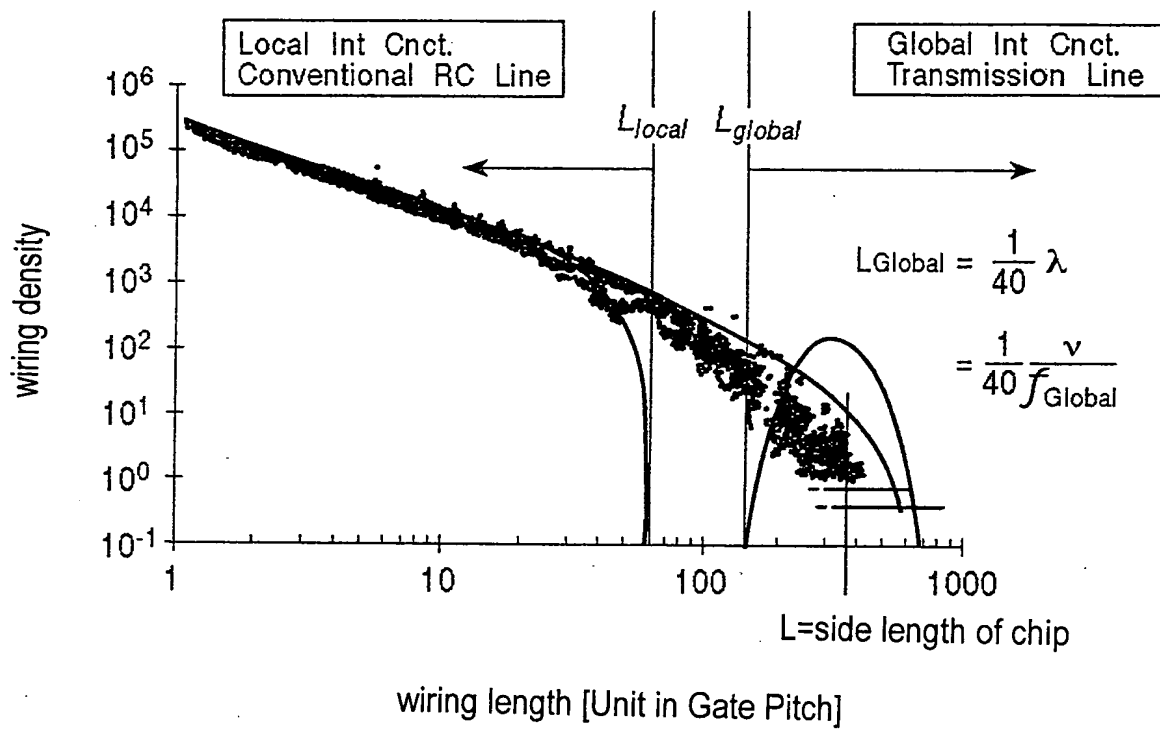
**Fig. 1**

Fig. 2

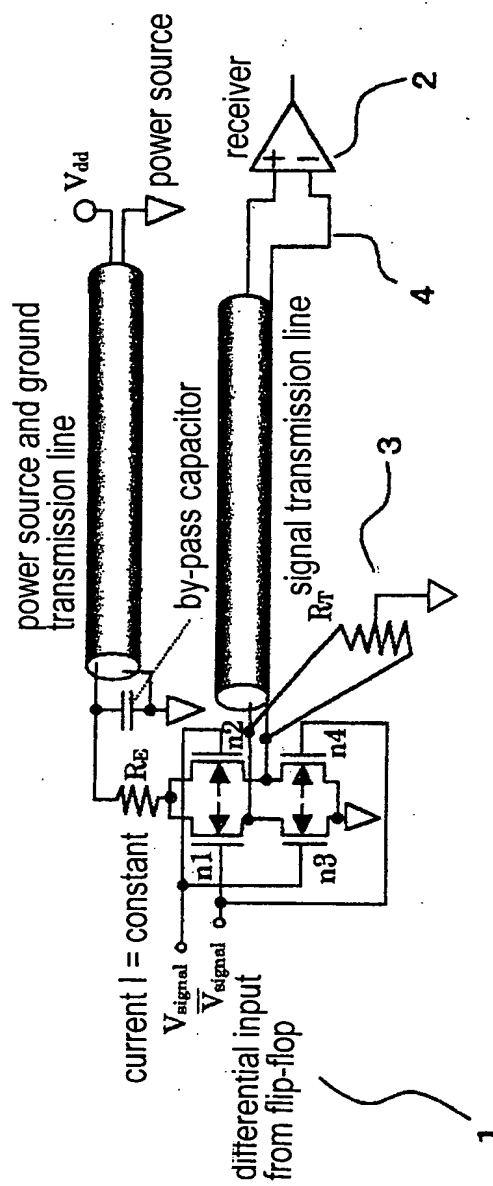


Fig. 3

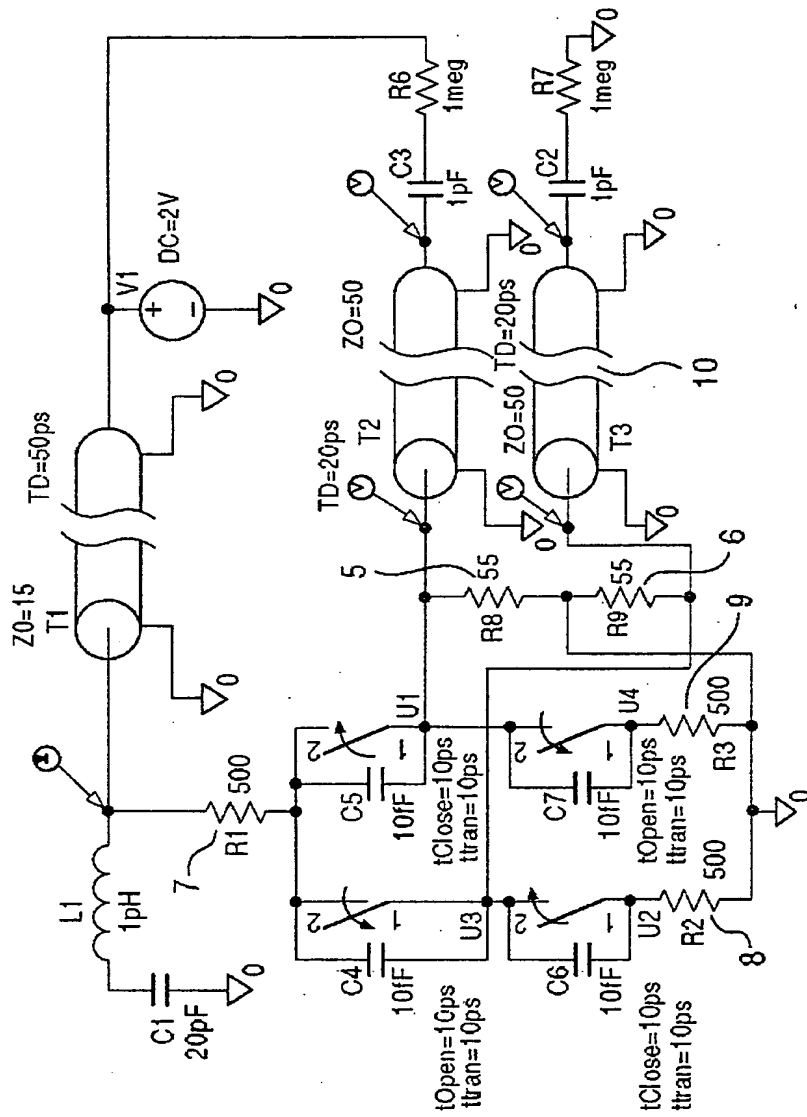


Fig. 4

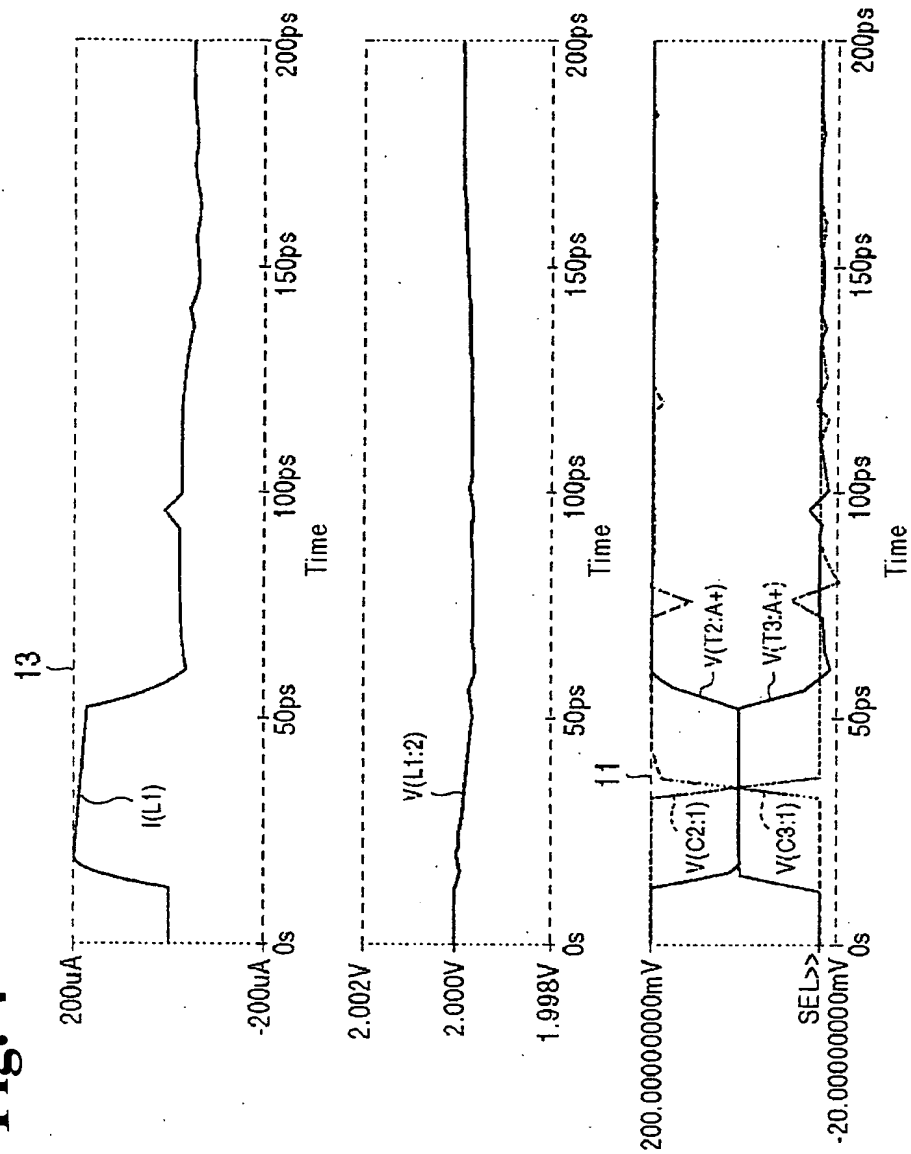


Fig. 5

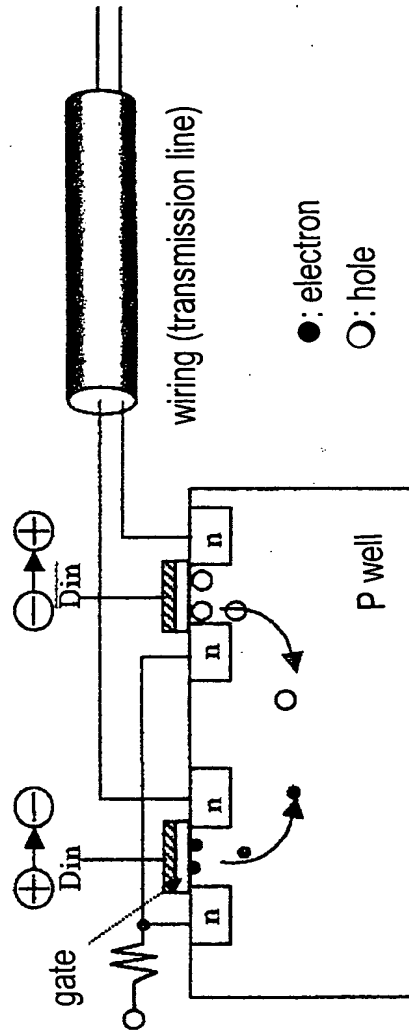
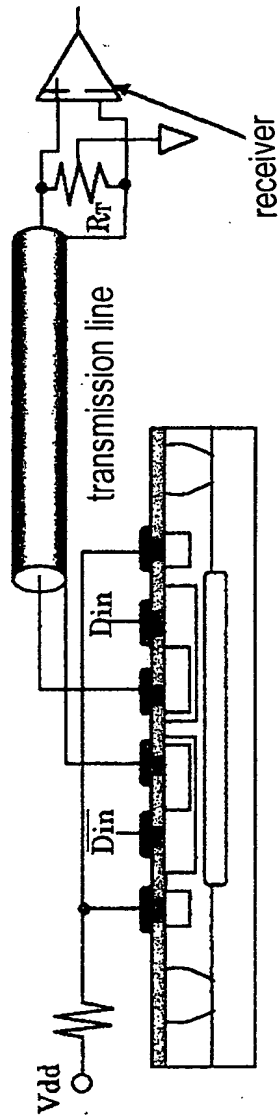


Fig. 6



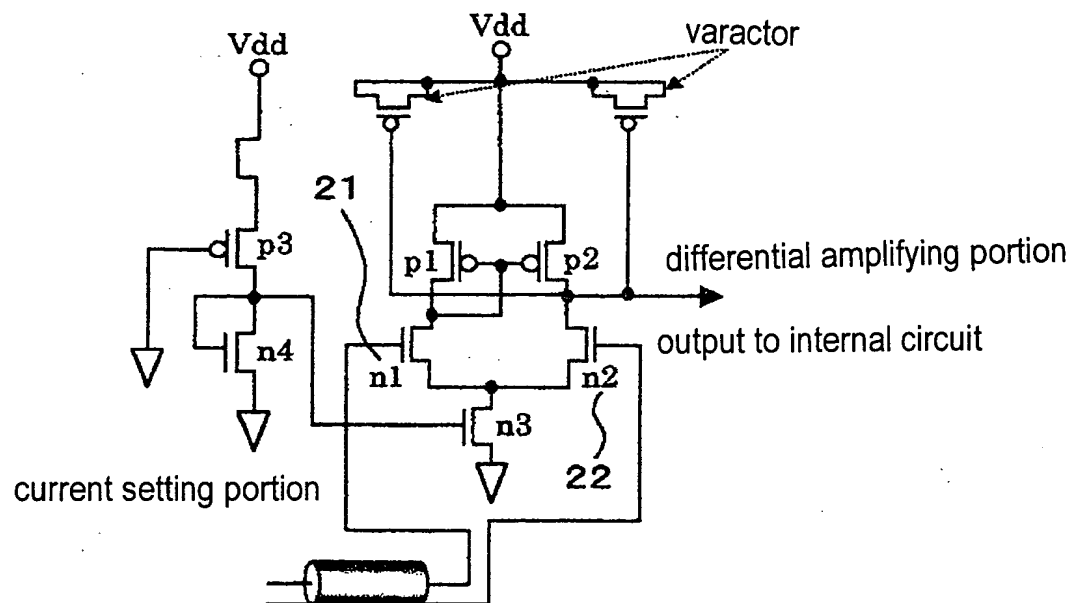
**Fig. 7**

Fig. 8

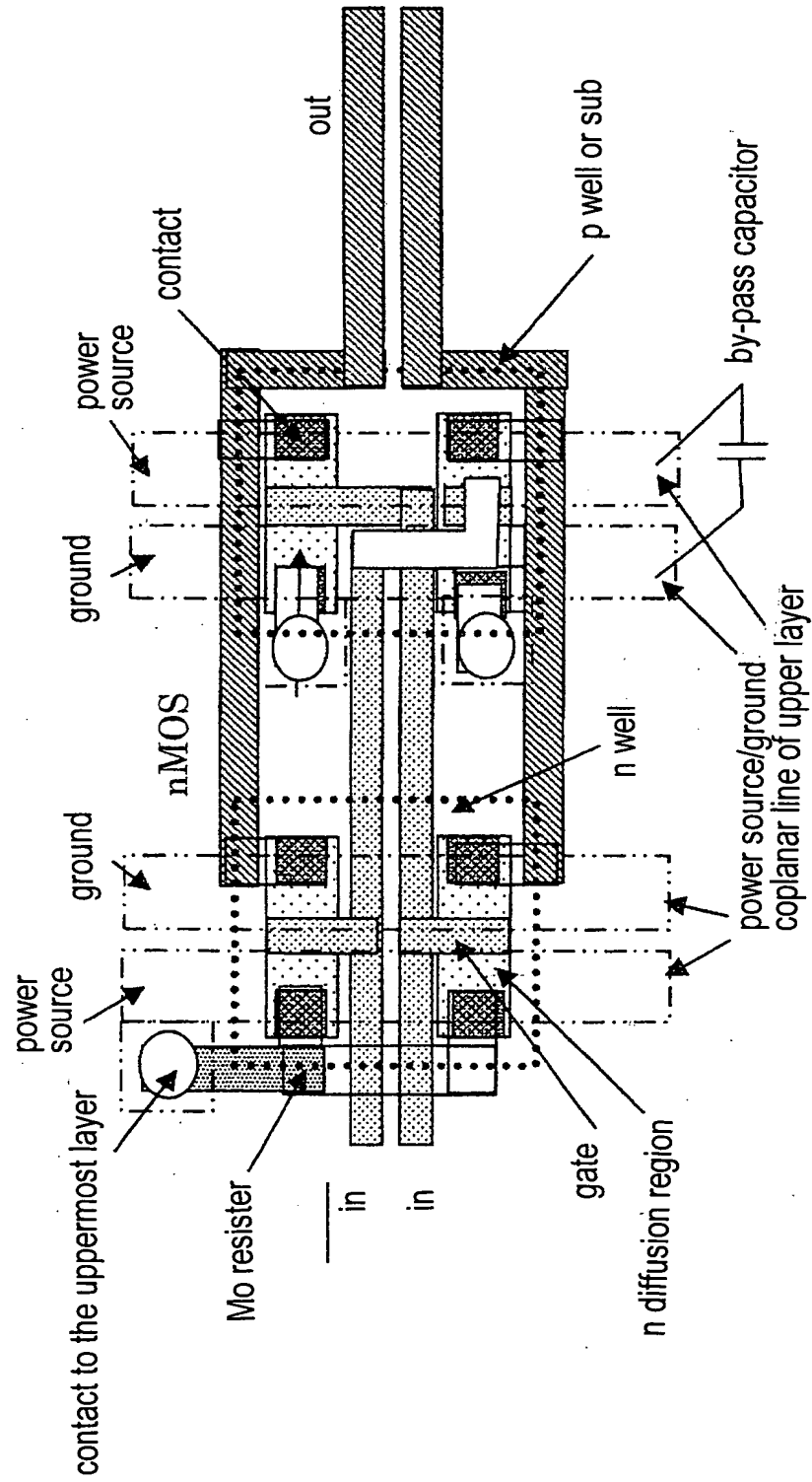
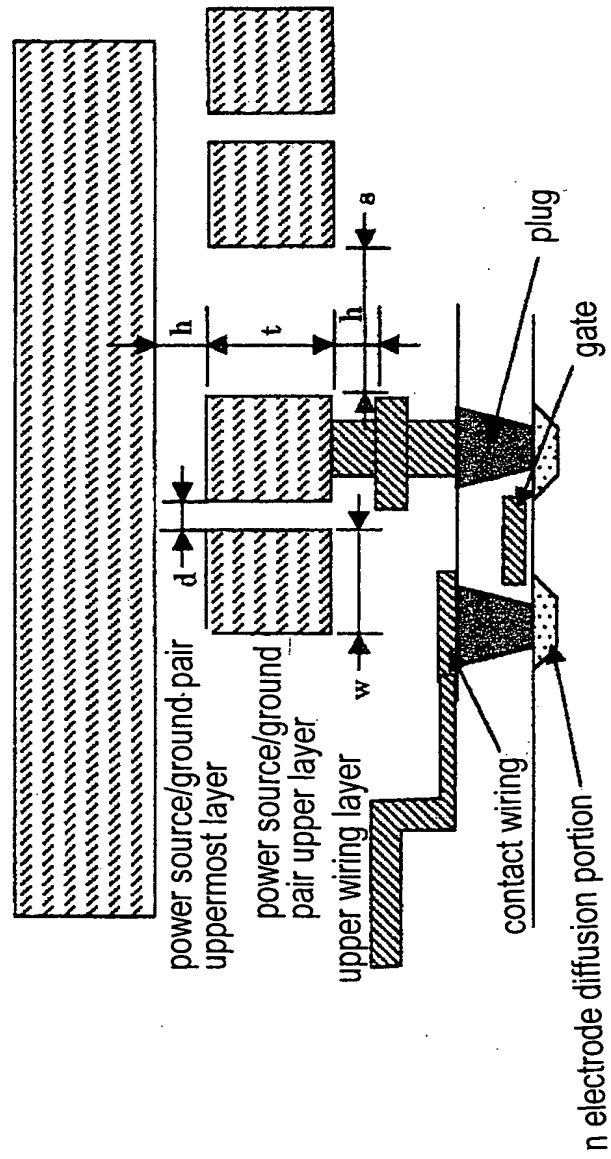
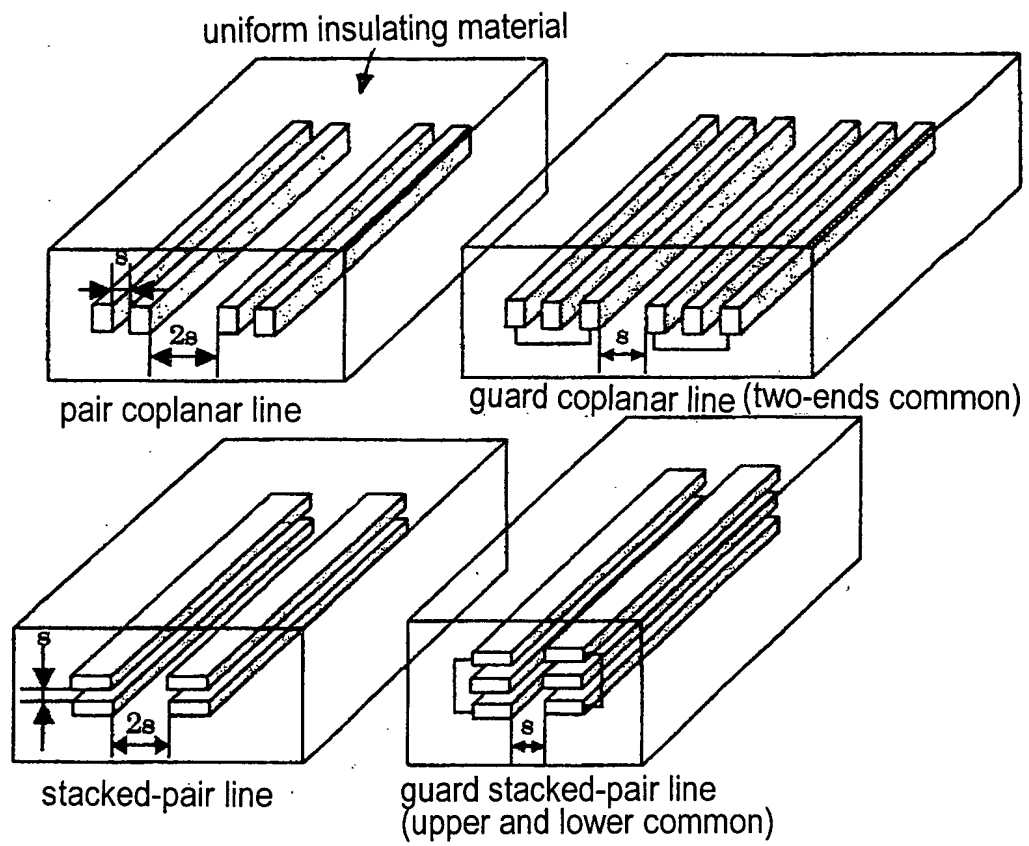


Fig. 9



**Fig. 10**

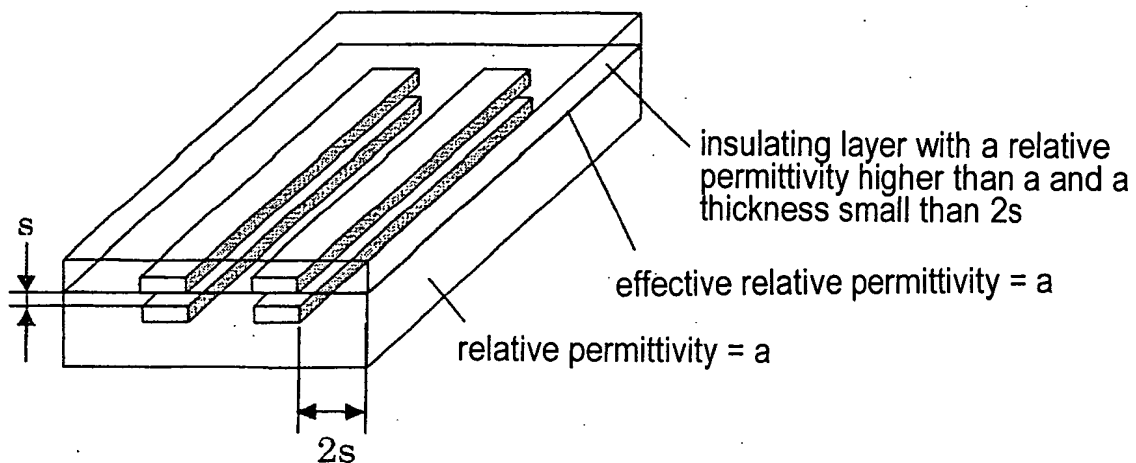
**Fig. 11**

Fig. 12(b)

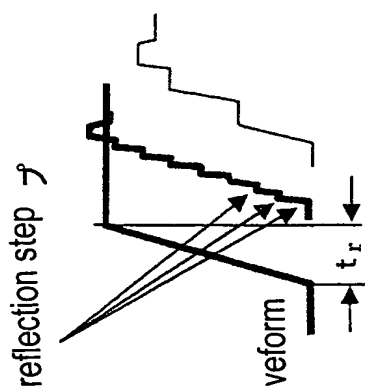


Fig. 12(a)

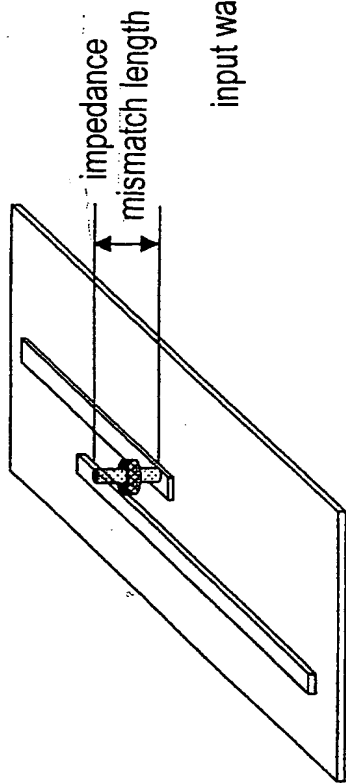
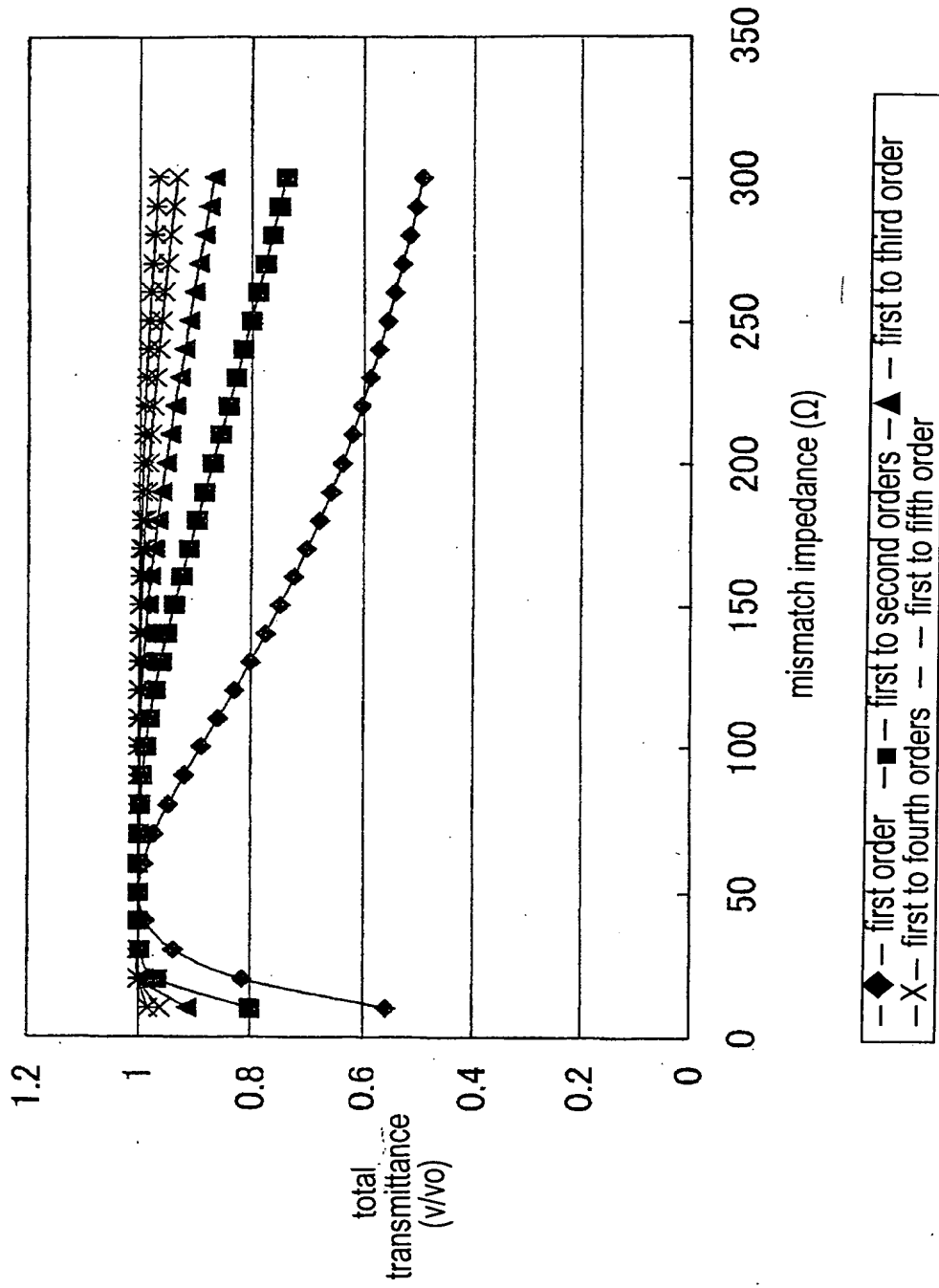
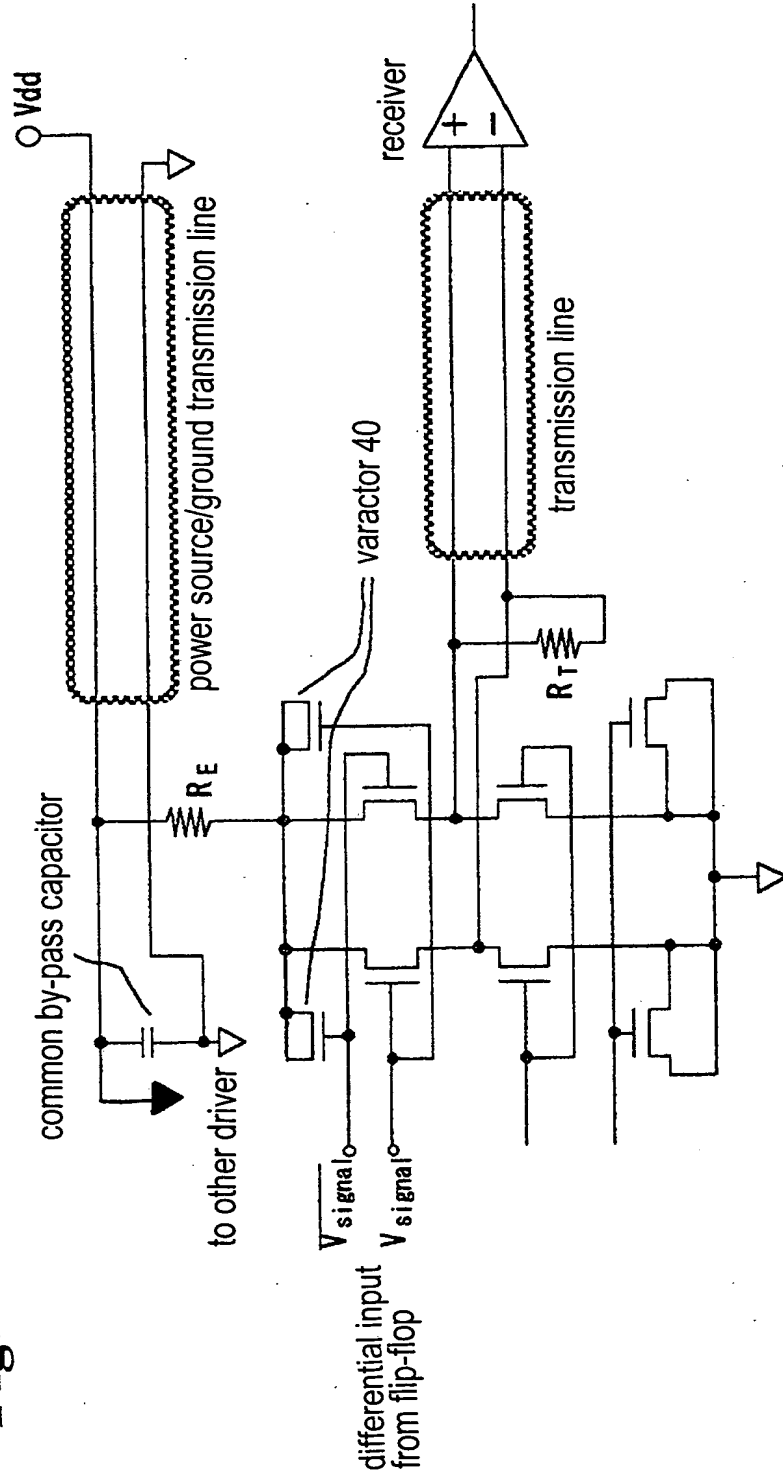
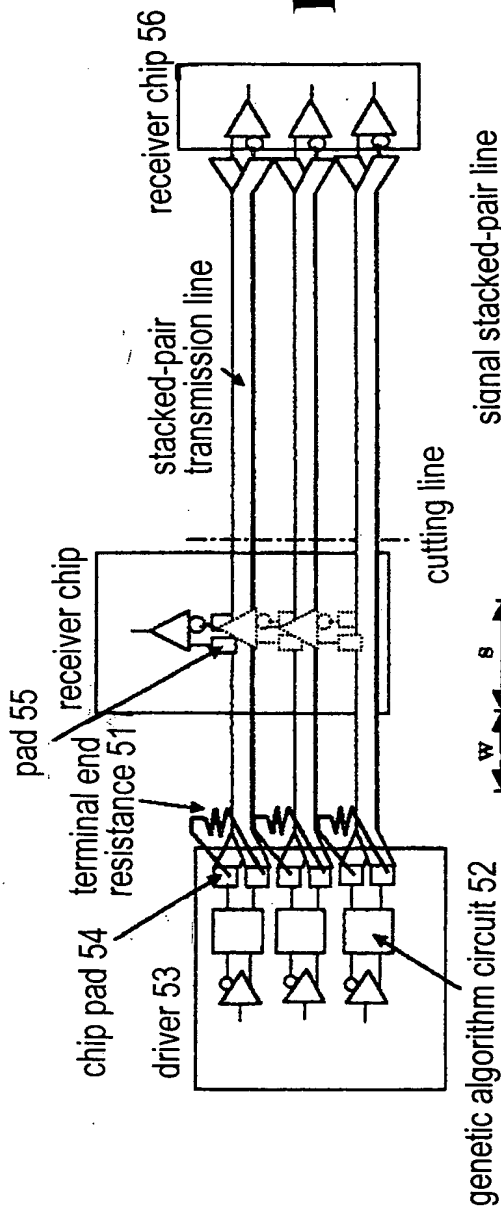


Fig. 13

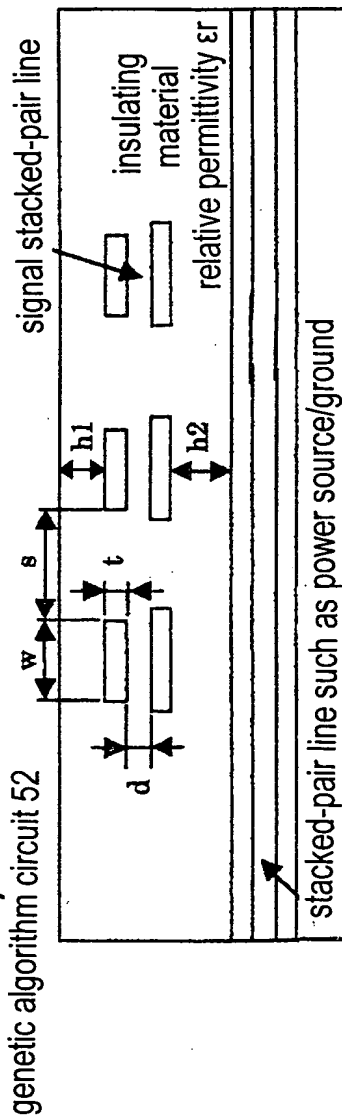


**Fig. 14**





**Fig. 15(a)**  
plan view



**Fig. 15(b)**  
sectional view

Fig. 16

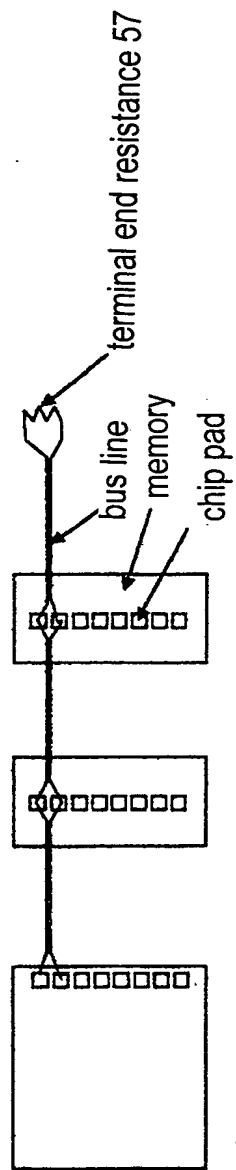
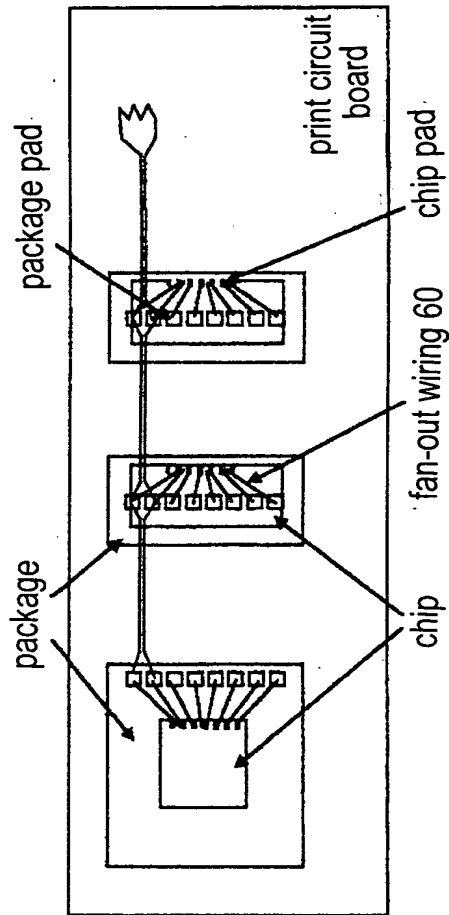
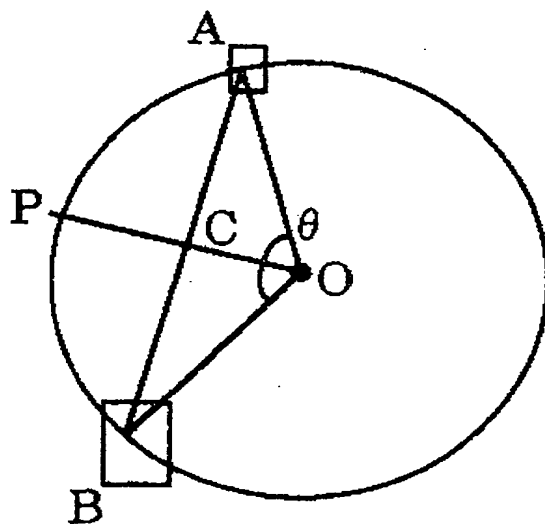


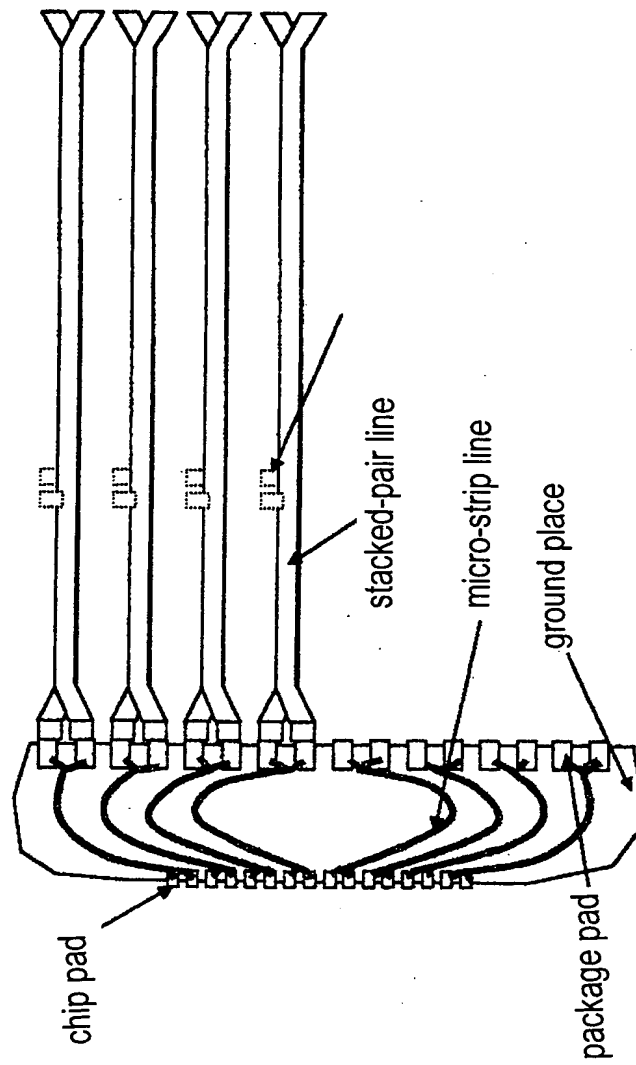
Fig. 17



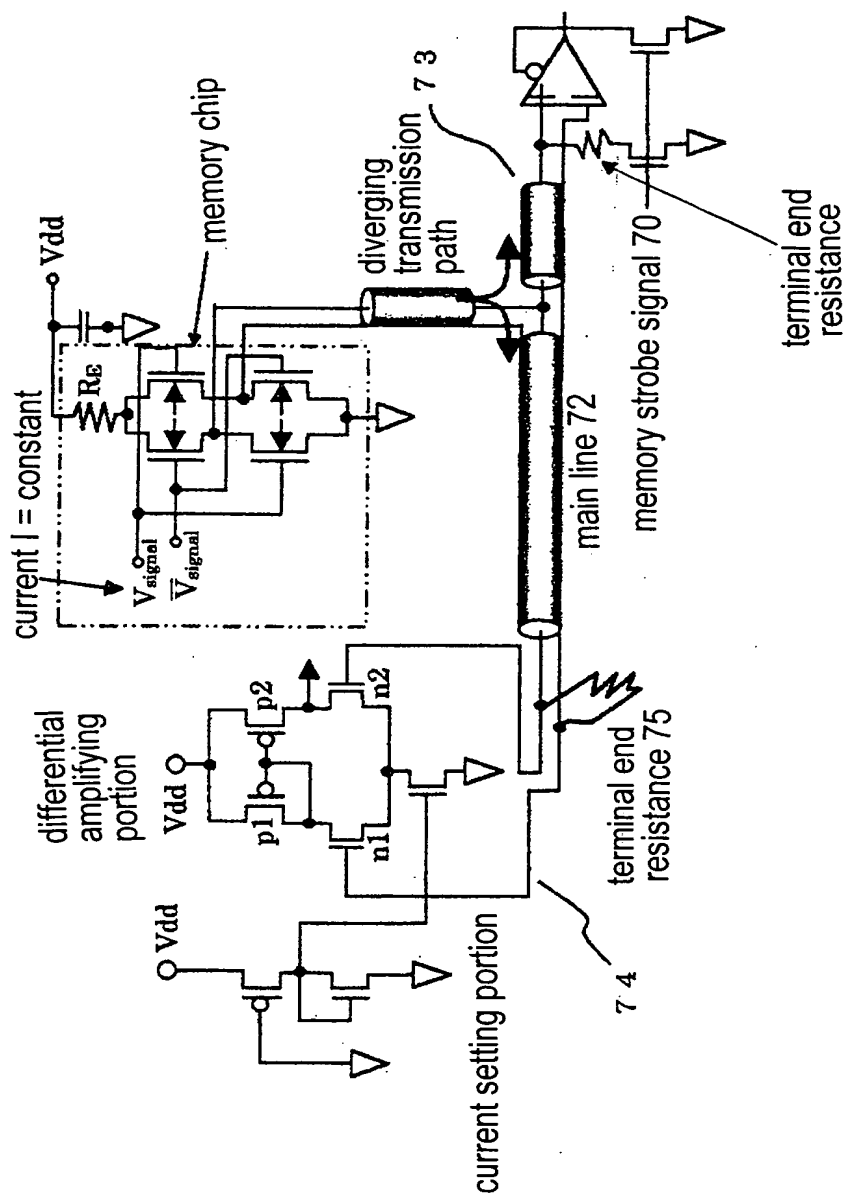
**Fig. 18**

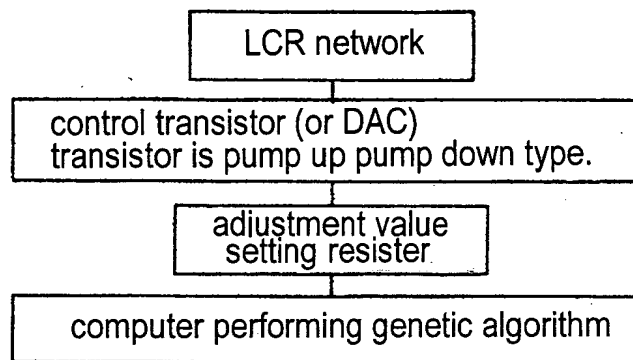
**Fig. 19**



**Fig. 20**

**Fig. 21**



**Fig. 22**

**Fig. 23**

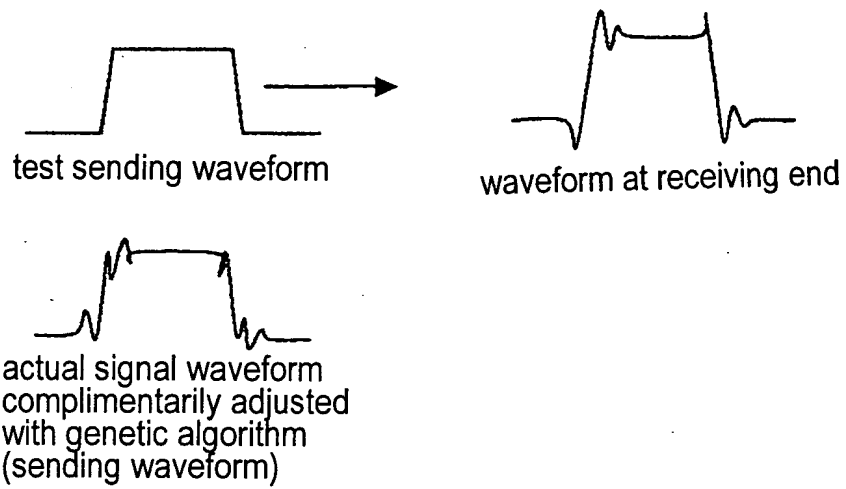
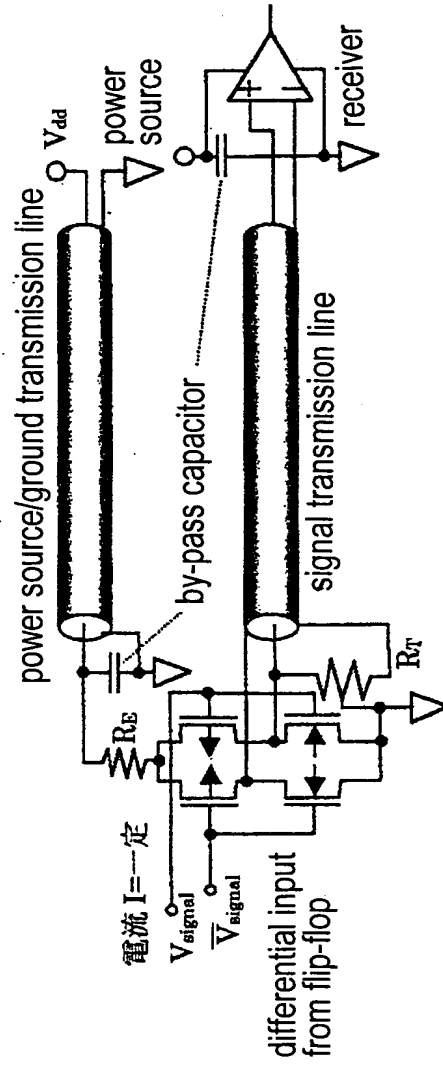
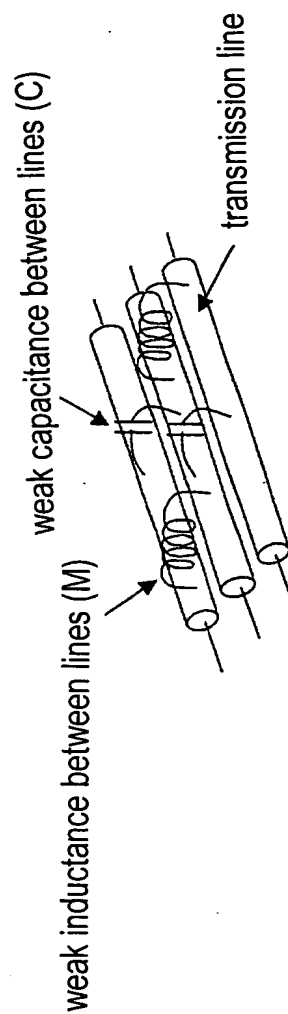


Fig. 24



**Fig. 25**

**Fig. 26**

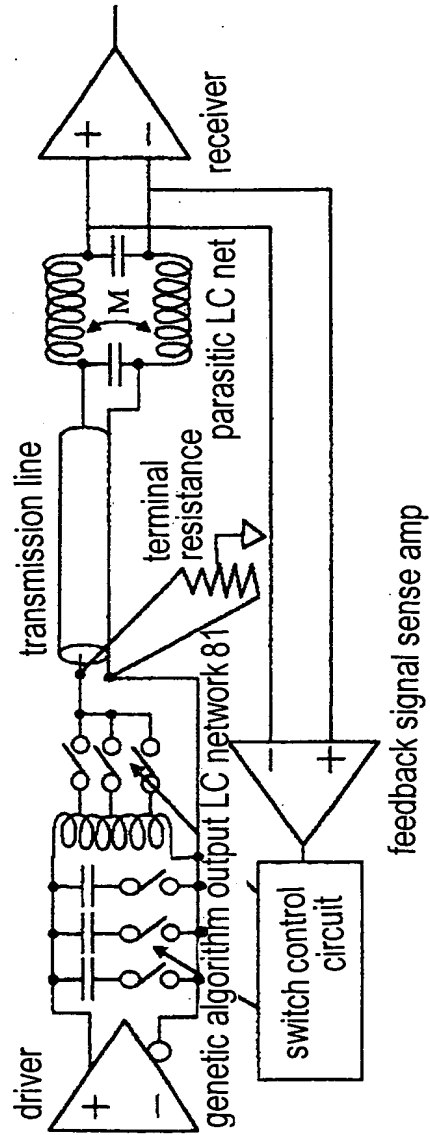
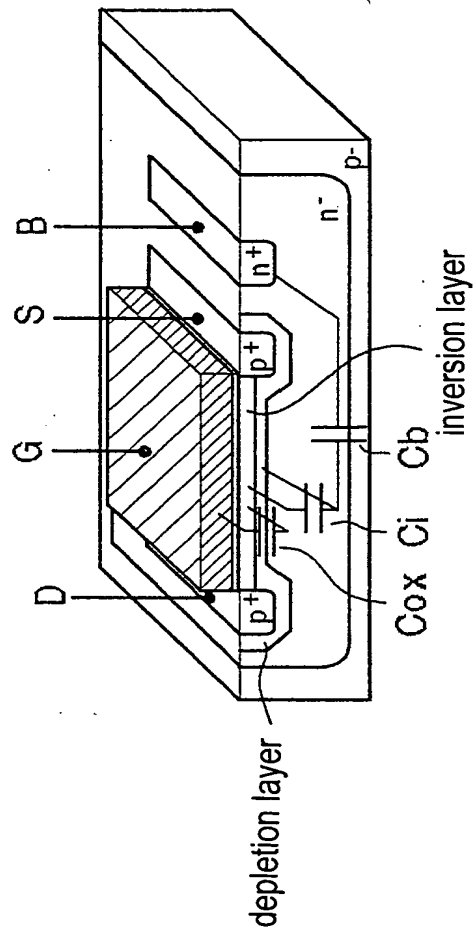
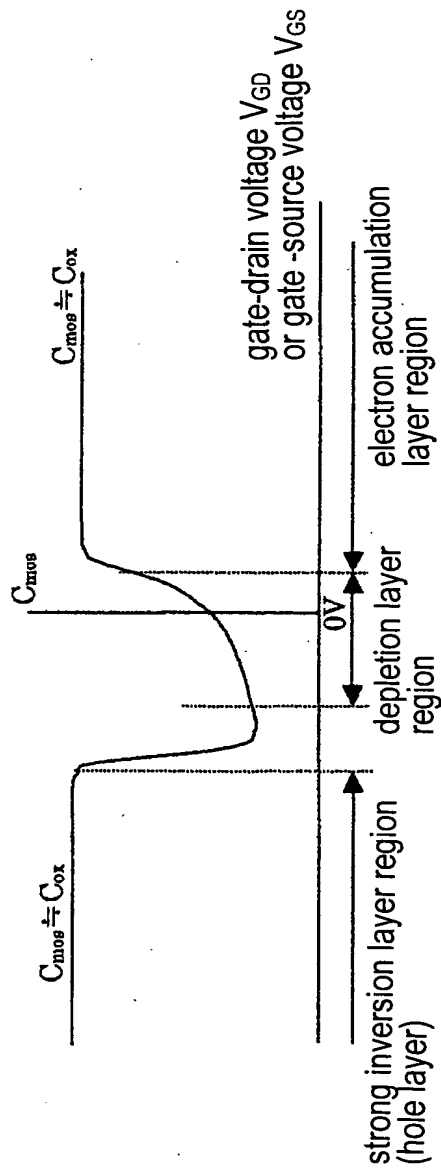


Fig. 27



**Fig. 28**

**Fig. 29**

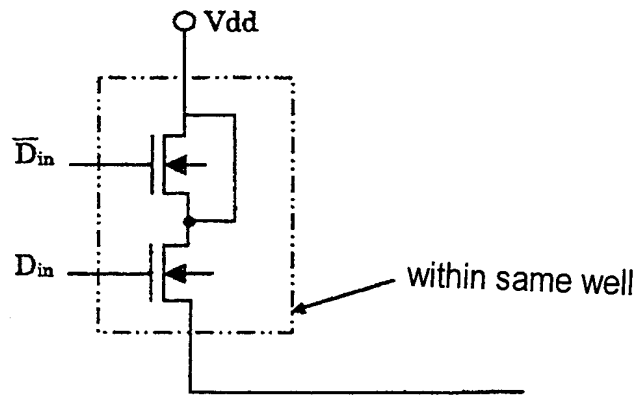


Fig. 30

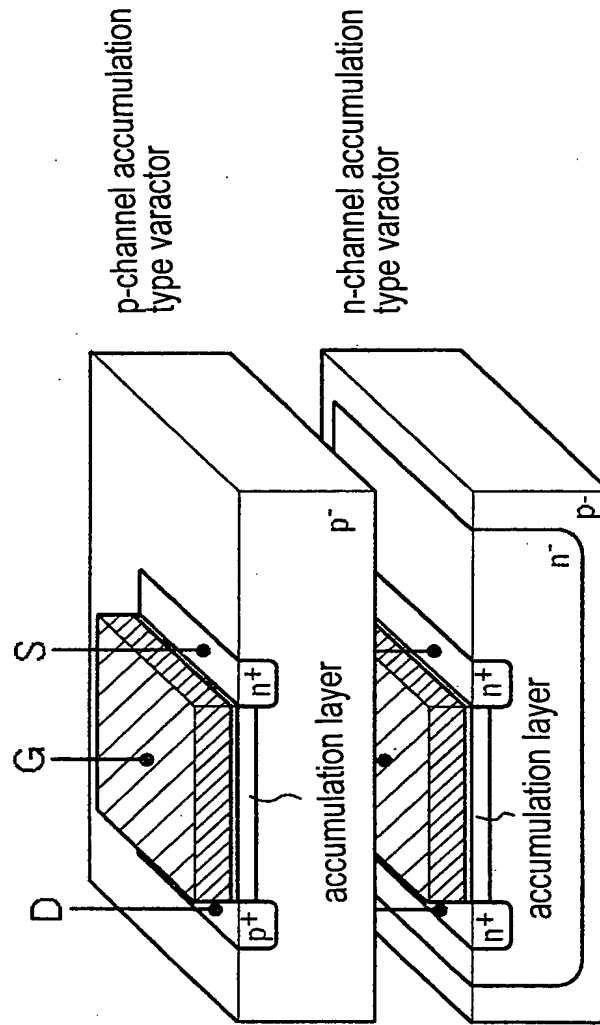
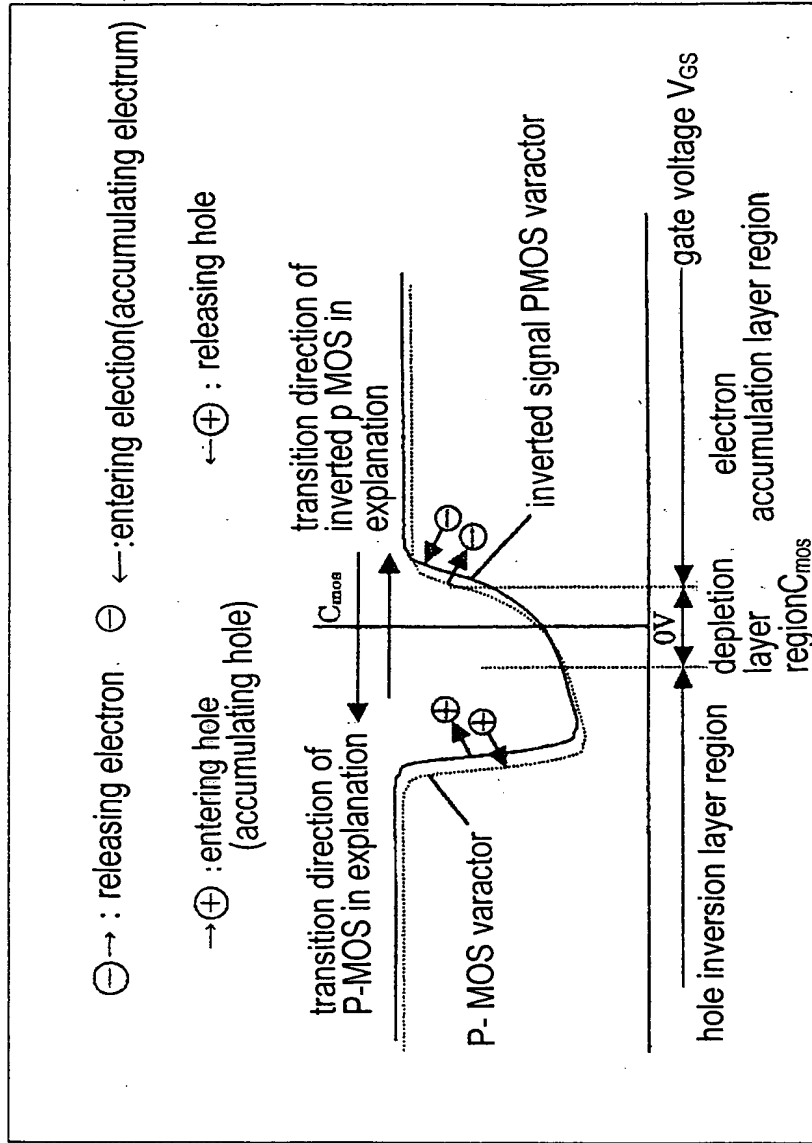


Fig. 31



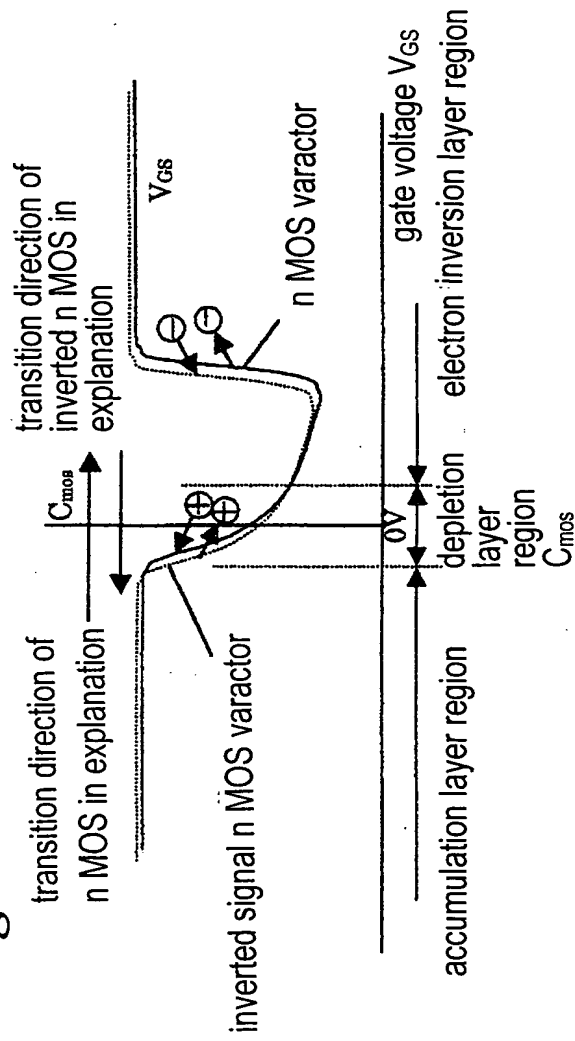
**Fig. 32**

Fig. 33

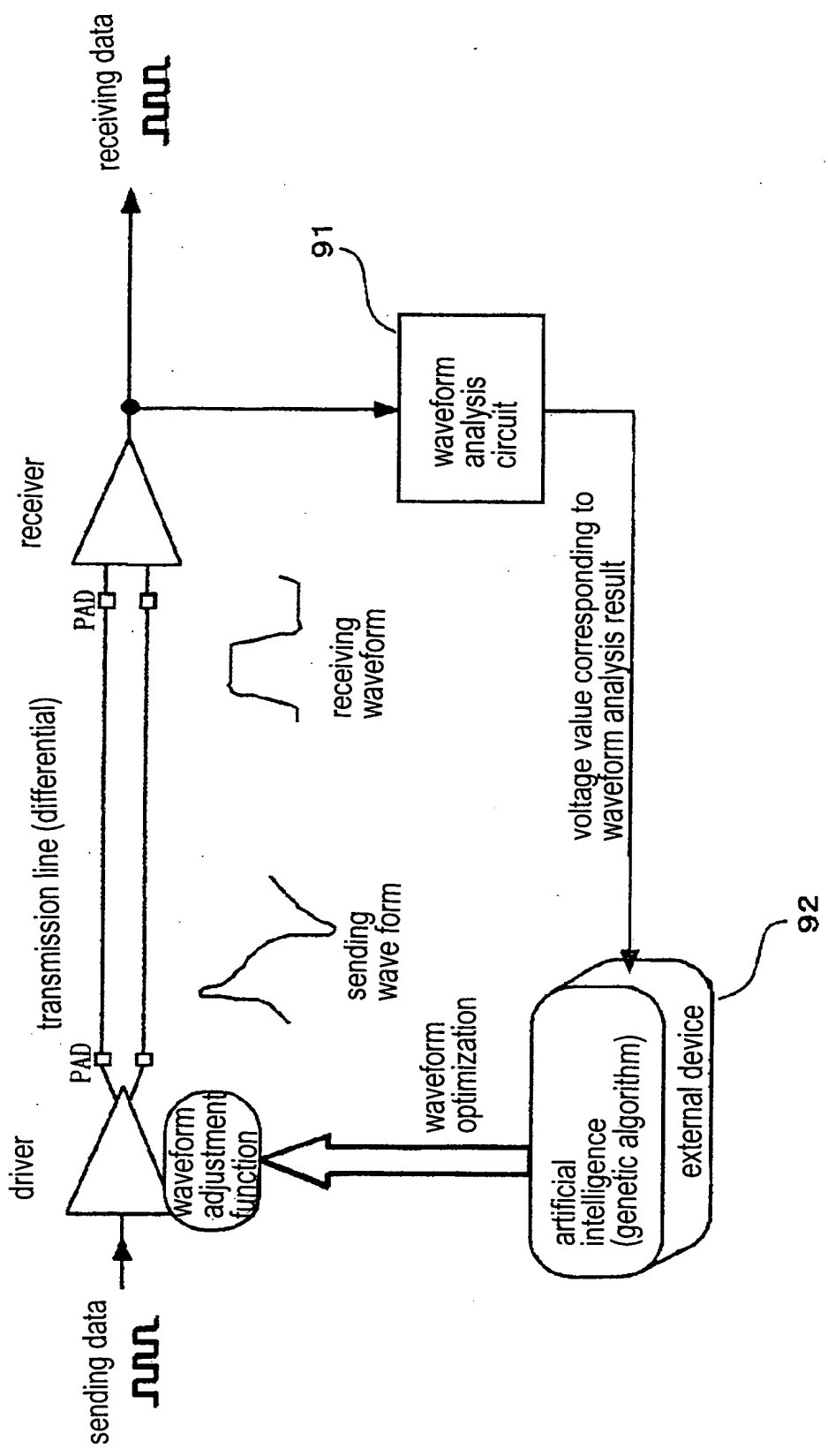
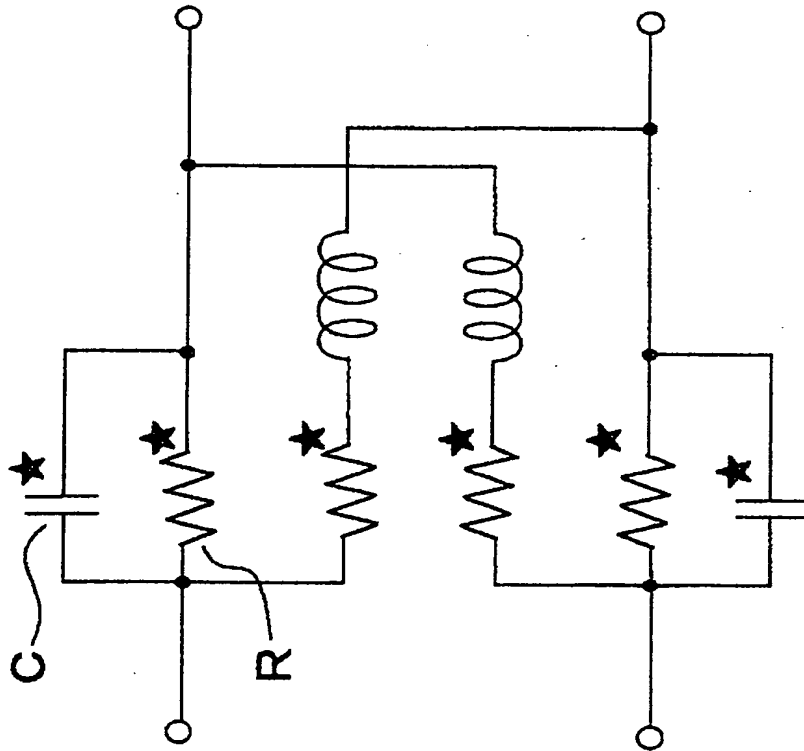
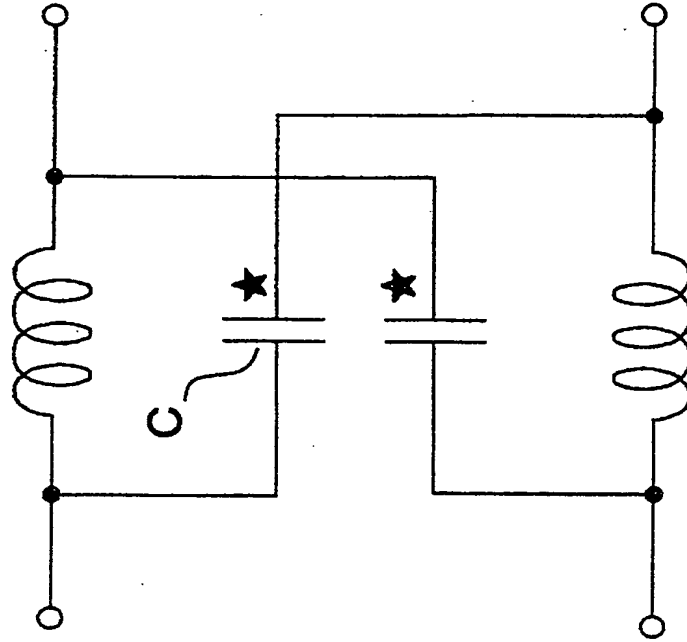


Fig. 34(a)



(adjustment based on amplitude)

Fig. 34(b)



★: variable value

(adjustment based on phase)

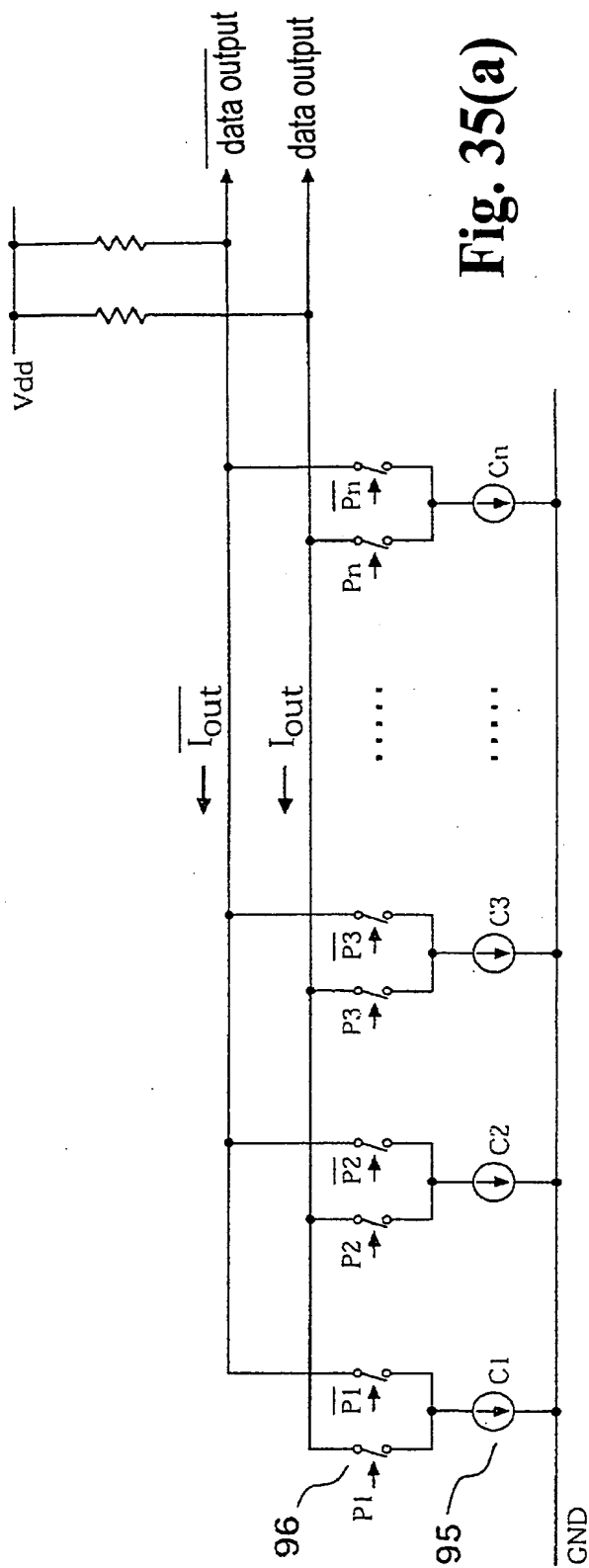


Fig. 35(a)

$C_0 \sim C_n$  are variable (externally adjusted) analog current values (direct current)

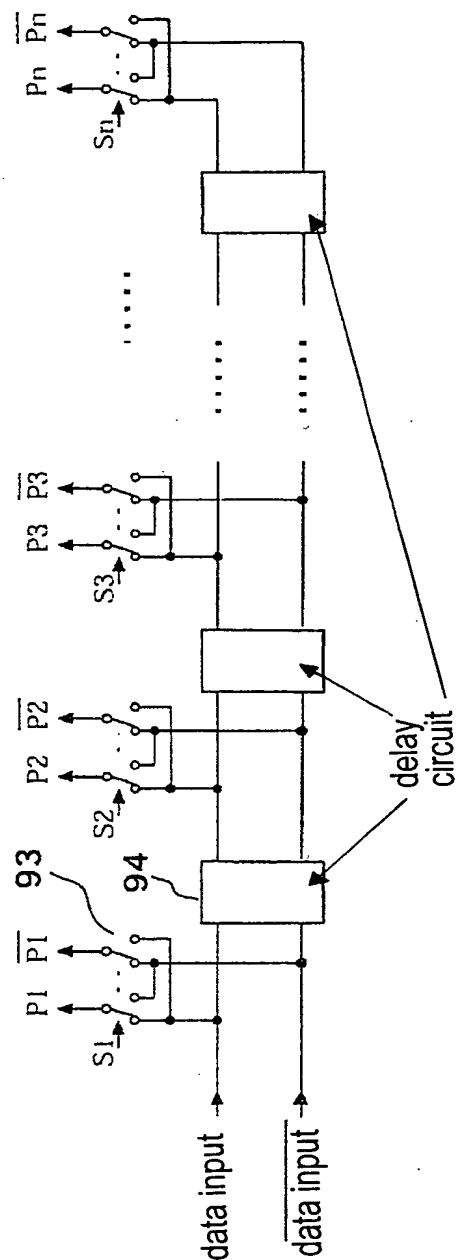
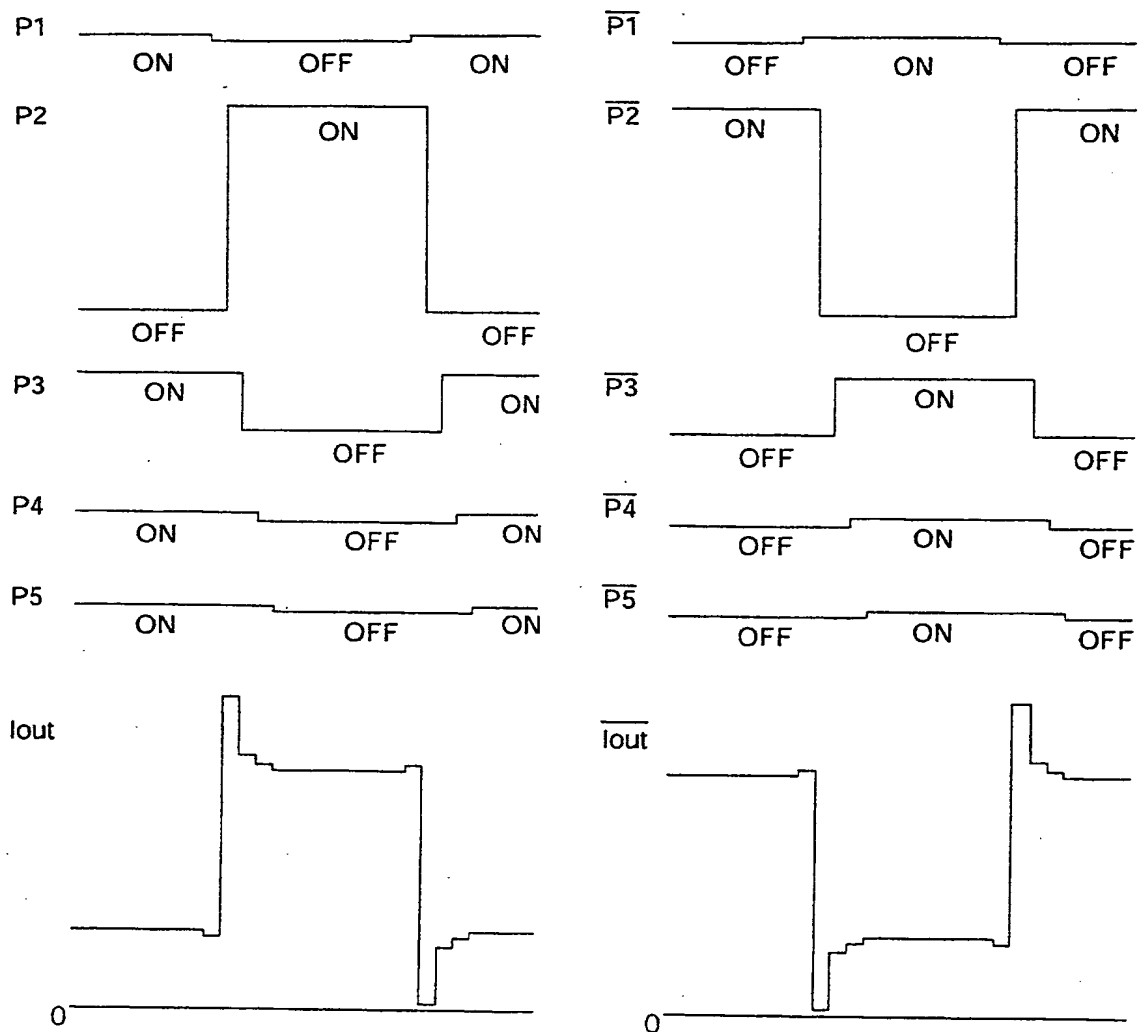


Fig. 35(b)

**Fig. 36**

waveform of  $i_{out}$  in which a current waveform of each switch and a corresponding current are combined:



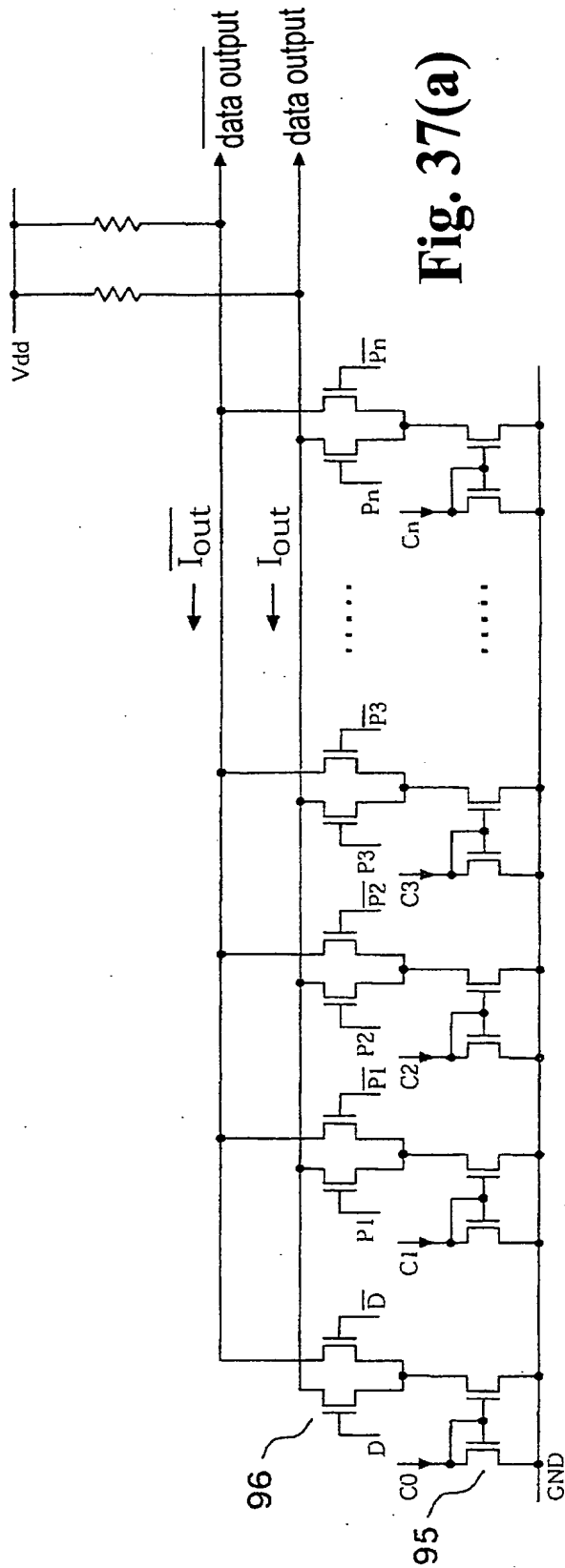


Fig. 37(a)

C0 ~ Cn are variable (externally adjusted) analog current values (direct current)

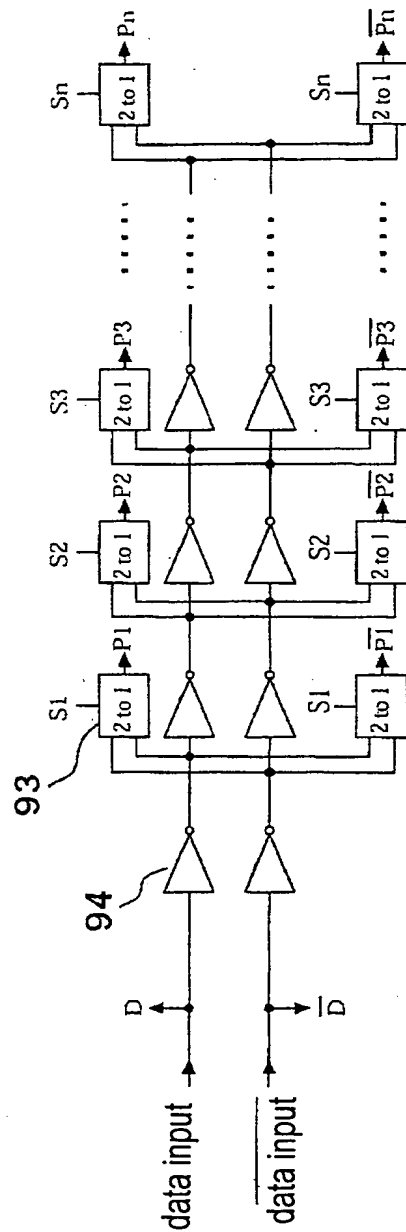


Fig. 37(b)

Fig. 38

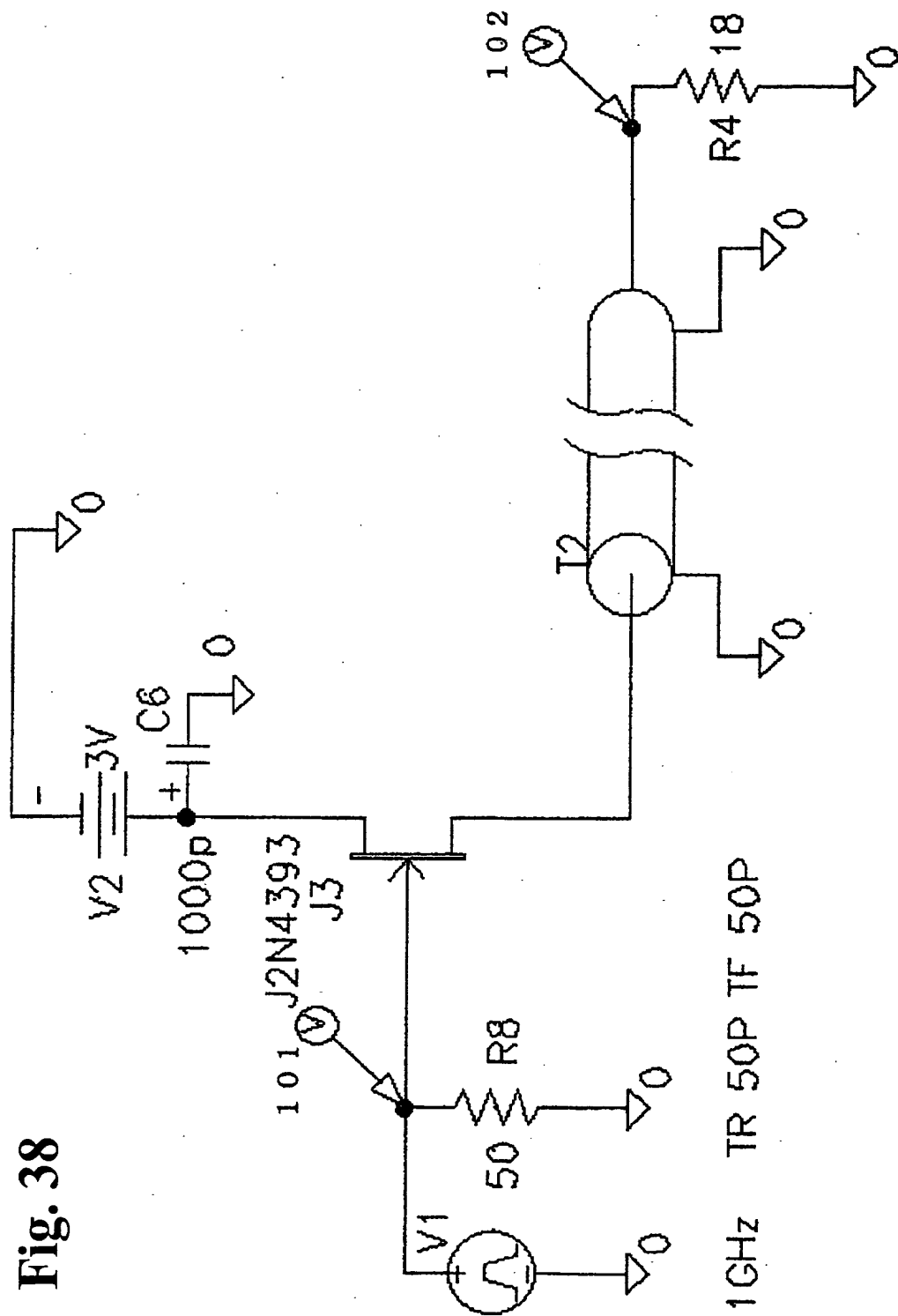


Fig. 39

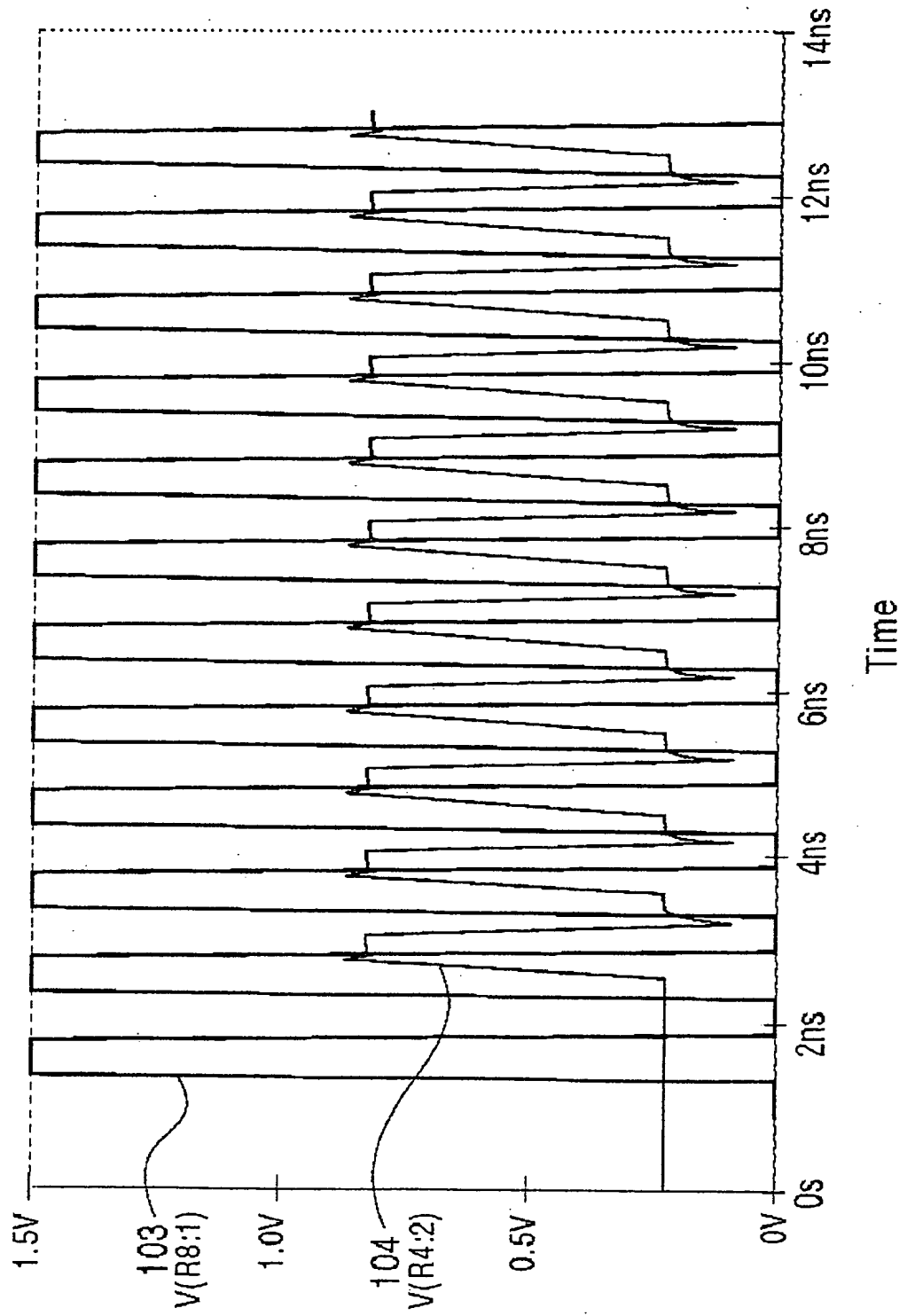
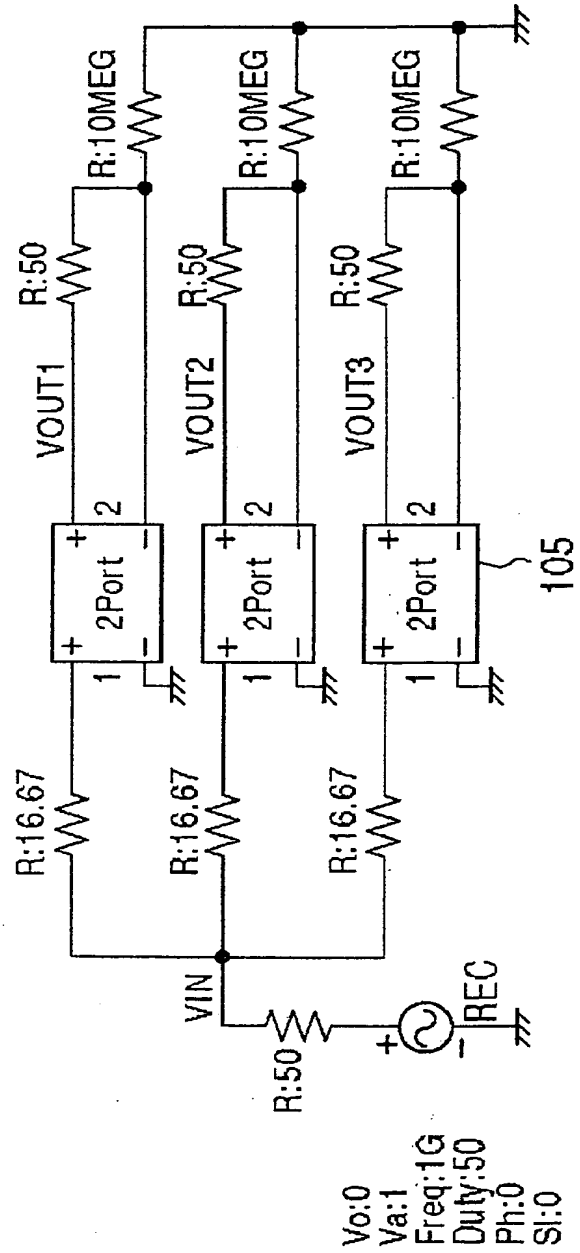
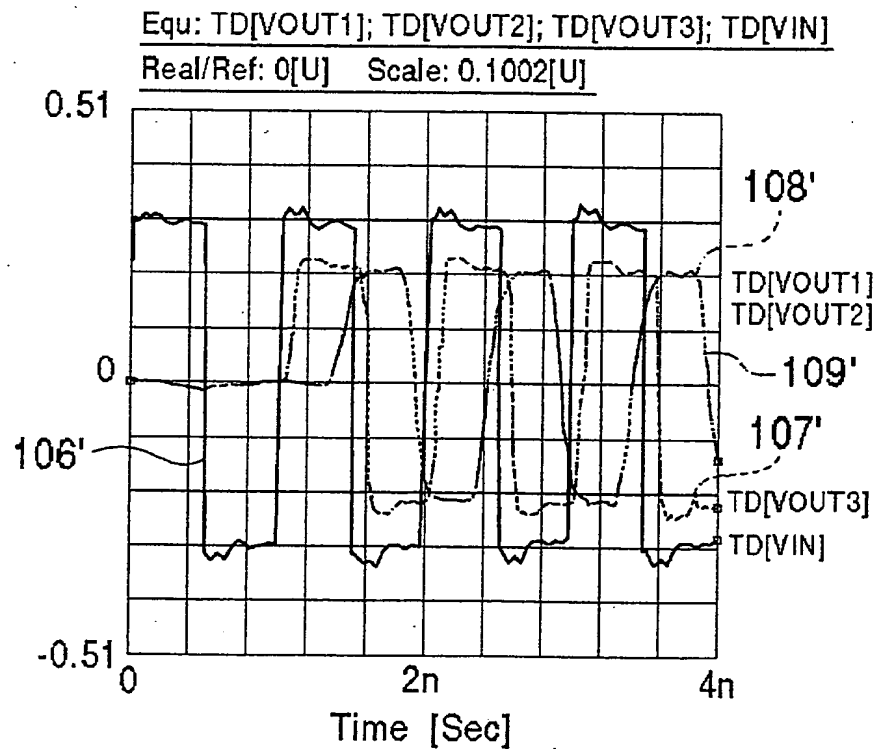
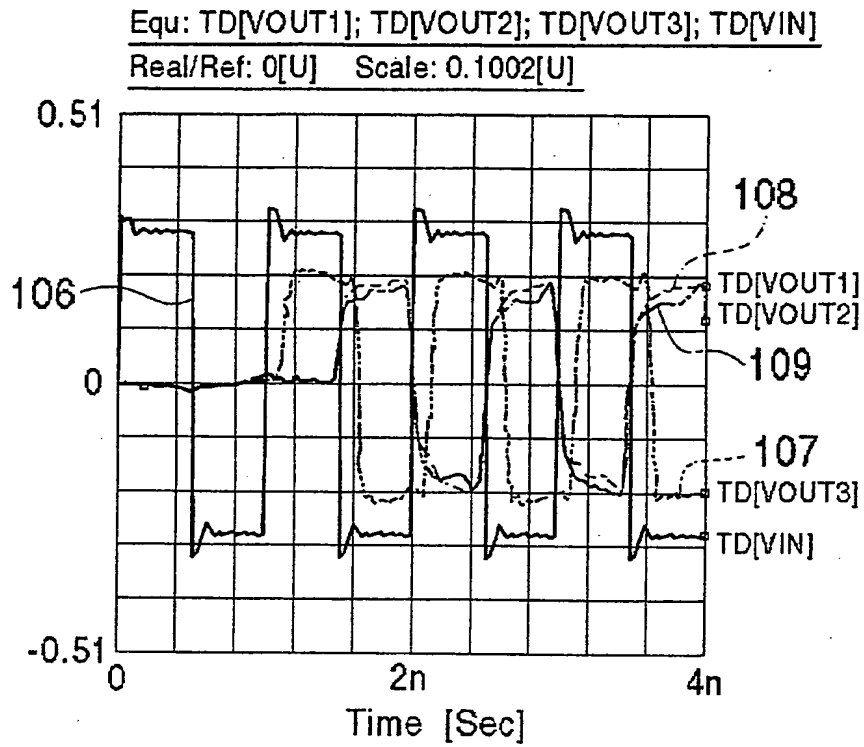


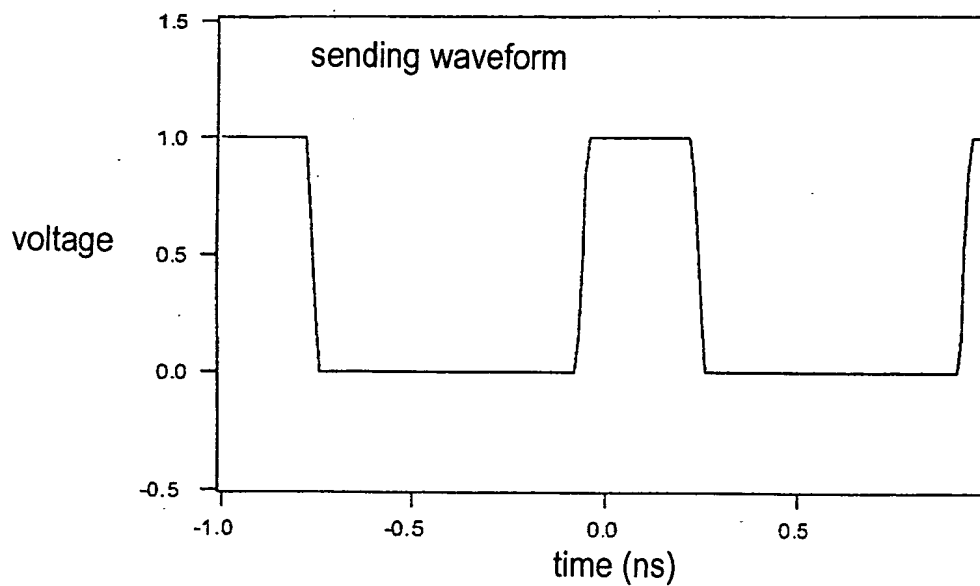
Fig. 40

Data:C:¥My\_Documents¥spara¥MS\_PL\_FR4¥MS\_1\_1\_2\_1.S2P  
 V:0  
 I:0  
 Data:C:¥My\_Documents¥spara¥MS\_PL\_BTREGIN¥msbp11s21.s2p  
 V:0  
 I:0  
 Data:C:¥My\_Documents¥spara¥aron¥Ms11s21.s2p  
 V:0  
 I:0

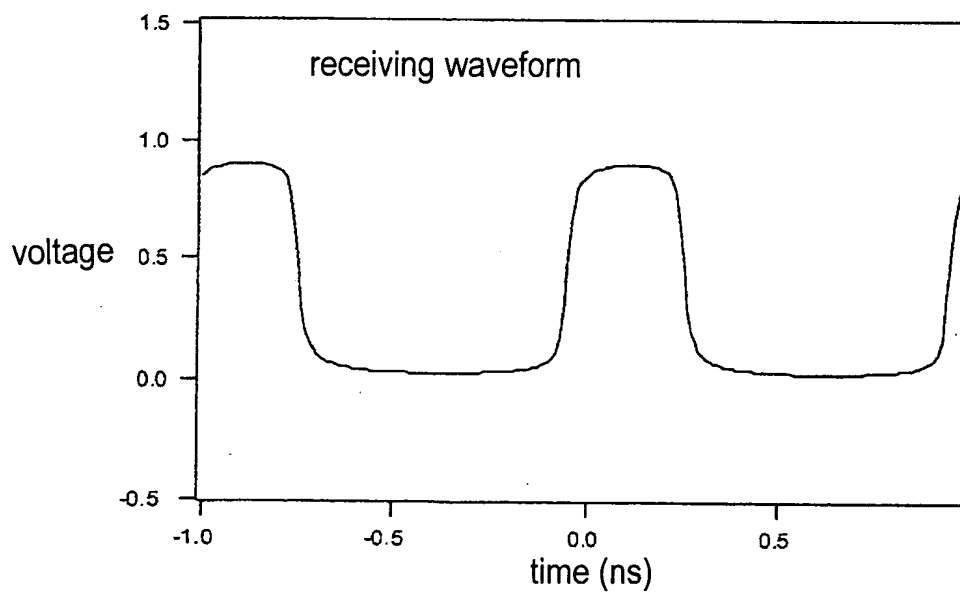


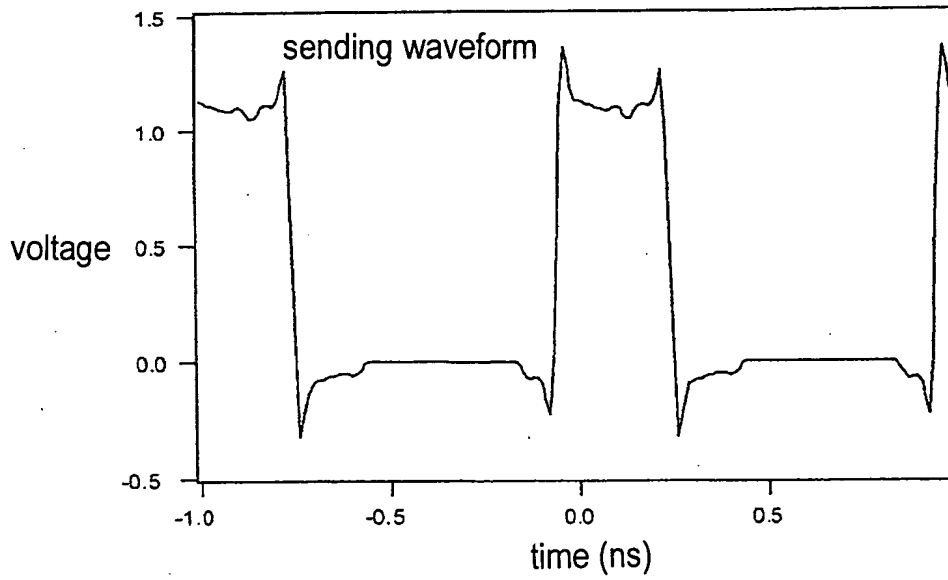
**Fig. 41**

**Fig. 42(a)**



**Fig. 42(b)**



**Fig. 43(a)****Fig. 43(b)**